SDAS122A – DECEMBER 1983 – REVISED JANUARY 1995

- Bidirectional Bus Transceivers in High-Density 20-Pin Packages
- Inverting Logic
- Package Options Include Plastic Small-Outline (DW) Packages, Ceramic Chip Carriers (FK), and Standard Plastic (N) and Ceramic (J) 300-mil DIPs

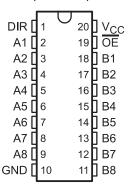
description

These octal bus transceivers are designed for asynchronous two-way communication between data buses. These devices transmit data from the A bus to the B bus or from the B bus to the A bus, depending upon the level at the direction-control (DIR) input. The output-enable (\overline{OE}) input can be used to disable the device so that the buses are effectively isolated.

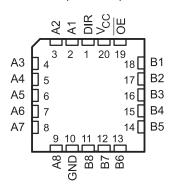
The -1 version of the SN74ALS640B is identical to the standard version, except that the recommended maximum I_{OL} for the -1 version is increased to 48 mA. There is no -1 version of the SN54ALS640B.

The SN54ALS640B and SN54AS640 are characterized for operation over the full military temperature range of -55°C to 125°C. The SN74ALS640B and SN74AS640 are characterized for operation from 0°C to 70°C.

SN54ALS640B, SN54AS640 . . . J PACKAGE SN74ALS640B, SN74AS640 . . . DW OR N PACKAGE (TOP VIEW)



SN54ALS640B, SN54AS640 . . . FK PACKAGE (TOP VIEW)



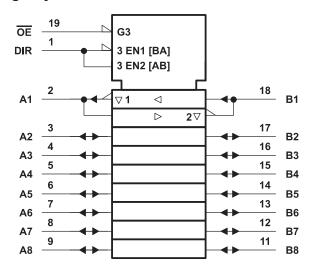
FUNCTION TABLE

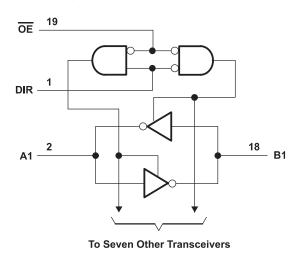
INP	UTS	ODEDATION
ŌE	DIR	OPERATION
L	L	B data to A bus
L	Н	A data to B bus
Н	X	Isolation

SDAS122A - DECEMBER 1983 - REVISED JANUARY 1995

logic symbol†

logic diagram (positive logic)





[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage, V _{CC}	
Input voltage, V _I : All inputs	7 V
I/O ports	
Operating free-air temperature range, T _A : SN54ALS640B	. −55°C to 125°C
SN74ALS640B	0°C to 70°C
Storage temperature range	. −65°C to 150°C

[‡] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

		SN54ALS640B			SN7	UNIT		
		MIN	NOM	MAX	MIN	NOM	MAX	UNII
Vcc	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage	2			2			V
VIL	Low-level input voltage			0.7			0.8	V
ЮН	High-level output current			-12			-15	mA
I _{OL}	Low-level output current			12			24	mA
							48§	MA
TA	Operating free-air temperature	-55		125	0		70	°C

 $[\]S$ Applies only to the -1 version and only if V $_{CC}$ is between 4.75 V and 5.25 V



SDAS122A - DECEMBER 1983 - REVISED JANUARY 1995

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

DADAMETED		TEST CONDITIONS		SN5	4ALS64	ЮB	SN7	UNIT			
	PARAMETER	TEST CONDITIONS		MIN	TYP	MAX	MIN	TYP	MAX	UNII	
٧ıK		V _{CC} = 4.5 V,	I _I = -18 mA			-1.5			-1.5	V	
		$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	$I_{OH} = -0.4 \text{ mA}$	V _{CC} -2			V _{CC} -2	<u> </u>			
\ _{\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\}			$I_{OH} = -3 \text{ mA}$	2.4	3.2		2.4	3.2		V	
VOH		V _{CC} = 4.5 V	$I_{OH} = -12 \text{ mA}$	2						V	
			$I_{OH} = -15 \text{ mA}$				2				
			I _{OL} = 12 mA		0.25	0.4		0.25	0.4		
VOL		V _{CC} = 4.5 V	I _{OL} = 24 mA					0.35	0.5	V	
			I _{OL} = 48 mA [‡]					0.35	0.5		
	Control inputs	V _{CC} = 5.5 V	∨ _I = 7 ∨			0.1			0.1	mA	
կ	A or B ports		V _I = 5.5 V			0.1			0.1	IIIA	
1	Control inputs	V	\/· = 2.7.\/			20			20		
۱н	A or B ports§	vCC = 5.5 v,	$V_{CC} = 5.5 \text{ V}, \qquad V_{I} = 2.7 \text{ V}$			20			20	μΑ	
1	Control inputs	V22 = 5 5 V	\/r = 0.4.\/			-0.1			-0.1	mA	
կ∟	A or B ports§	V _{CC} = 5.5 V,	$_{\rm C} = 5.5 \text{ V}, \qquad \qquad \text{V}_{\rm I} = 0.4 \text{ V}$			-0.1			-0.1	IIIA	
IoI		V _{CC} = 5.5 V,	V _O = 2.25 V	-20		-112	-30		-112	mA	
			Outputs high		19	50		19	45		
Icc	V _{CC} = 5.5 V	V _{CC} = 5.5 V	Outputs low		27	60		27	55	mA	
			Outputs disabled		28	55		28	50		

[†] All typical values are at V_{CC} = 5 V, T_A = 25°C.

switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V ₍ C ₁ R' R: T ₂	UNIT			
			SN54AL	S640B	SN74AL		
					MIN	MAX	
t _{PLH}	A or B	B or A	2	14	2	11	ns
t _{PHL}	AOIB	BOLA	2	13	2	10	115
^t PZH		A D	4	25	4	21	ns
t _{PZL}	OE	OE A or B	5	27	5	24	115
^t PHZ	ŌĒ	A or B	2	12	2	10	ns
t _{PLZ}	OL	AOIB	3	20	3	15	113

[#]For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.



[‡] Applies only to the -1 version and only if V_{CC} is between 4.75 V and 5.25 V

 $[\]S$ For I/O ports, the parameters I $_{
m IH}$ and I $_{
m IL}$ include the off-state output current.

The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, IOS.

SDAS122A - DECEMBER 1983 - REVISED JANUARY 1995

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V _{CC}		 7 V
Input voltage, V _I : All inputs		 7 V
I/O ports		 5.5 V
Operating free-air temperature range, TA		
, , ,	SN74AS640	 0°C to 70°C
Storage temperature range		-65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

		SN54AS640			SN74AS640			UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage	2			2			V
VIL	Low-level input voltage			8.0			0.8	V
Іон	High-level output current			-12			-15	mA
loL	Low-level output current			48			64	mA
TA	Operating free-air temperature	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEOT 001	NOTIONS	SN	54AS64	10	SN	LINUT			
		IESI COI	TEST CONDITIONS		TYP‡	MAX	MIN	TYP‡	MAX	UNIT	
٧ıĸ		V _{CC} = 4.5 V,	I _I = –18 mA			-1.2			-1.2	V	
		V _{CC} = 4.5 V,	$I_{OH} = -2 \text{ mA}$	V _{CC} -2							
		$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	$I_{OH} = -2 \text{ mA}$				V _{CC} -2	2			
Vон			$I_{OH} = -3 \text{ mA}$	2.4	3.2		2.4	3.2		V	
		V _{CC} = 4.5 V	$I_{OH} = -12 \text{ mA}$	2.4							
			$I_{OH} = -15 \text{ mA}$				2.4				
\/a.		V _{CC} = 4.5 V	I _{OL} = 48 mA		0.3	0.55				· v	
VOL		VCC - 4.5 V	I _{OL} = 64 mA					0.35	0.55		
I	Control inputs	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	V _I = 7 ∨			0.1			0.1	mA	
կ	A or B ports	V _{CC} = 5.5 V	V _I = 5.5 V			0.1			0.1	IIIA	
l	Control inputs	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	V: = 2.7.V			20			20		
11H	A or B ports§	$V_{CC} = 5.5 \text{ V},$	V, V _I = 2.7 V			70			70	μΑ	
le.	Control inputs	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	V _I = 0.4 V			-0.5			-0.5	mA	
կ∟	A or B ports§	V _{CC} = 5.5 V,	V - 0.4 V	-0.75				-0.75	IIIA		
Io¶		V _{CC} = 5.5 V,	V _O = 2.25 V	-50		-150	-50		-150	mA	
			Outputs high		37	58		37	58		
Icc		V _{CC} = 5.5 V	Outputs low		78	123		78	123	mA	
			Outputs disabled		51	80		51	80		

 $[\]pm$ All typical values are at V_{CC} = 5 V, T_A = 25°C.

[¶]The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, IOS.



[§] For I/O ports, the parameters I_{IH} and I_{IL} include the off-state output current.

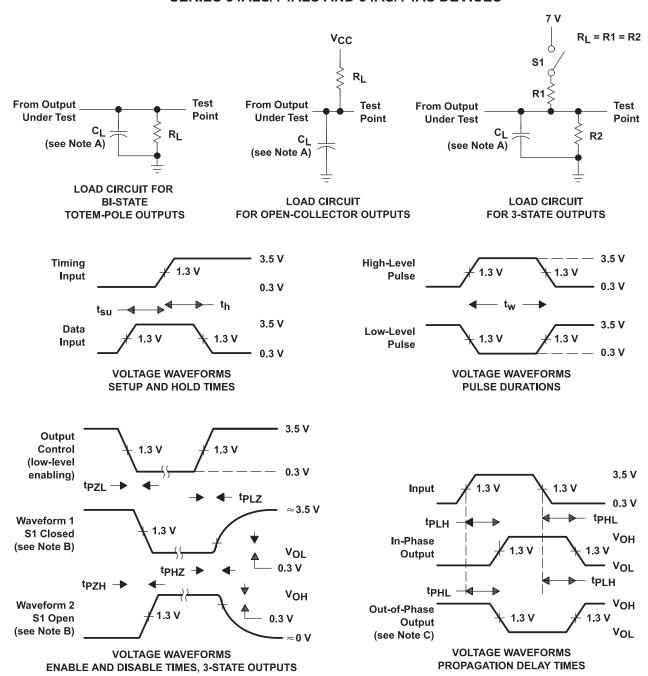
SN54ALS640B, SN54AS640, SN74ALS640B, SN74AS640 OCTAL BUS TRANSCEIVERS WITH 3-STATE OUTPUTS SDAS122A - DECEMBER 1983 - REVISED JANUARY 1995

switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _C C _L R1 R2 T _A	UNIT			
			SN54AS640		SN74AS640		
			MIN	MAX	MIN	MAX	
t _{PLH}	A B		1	8	2	7	
t _{PHL}	A or B	B or A	1	7	2	6	ns
^t PZH	ŌĒ		2	10	2	8	ns
t _{PZL}	OE	A or B	2	12	2	10	115
t _{PHZ}	ŌĒ	A or B	2	9	2	8	ns
t _{PLZ}	OE .	AUID	2	16	2	13	115

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

PARAMETER MEASUREMENT INFORMATION SERIES 54ALS/74ALS AND 54AS/74AS DEVICES



NOTES: A. C_L includes probe and jig capacitance.

- B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- C. When measuring propagation delay items of 3-state outputs, switch S1 is open.
- D. All input pulses have the following characteristics: PRR \leq 1 MHz, $t_r = t_f = 2$ ns, duty cycle = 50%.
- E. The outputs are measured one at a time with one transition per measurement.

Figure 1. Load Circuits and Voltage Waveforms

