

NPN high-voltage transistor

PMBT5551

FEATURES

- Low current (max. 300 mA)
- High voltage (max. 160 V).

APPLICATIONS

- General purpose
- Telephony.

DESCRIPTION

NPN high-voltage transistor in a SOT23 plastic package.
PNP complement: PMBT5401.

MARKING

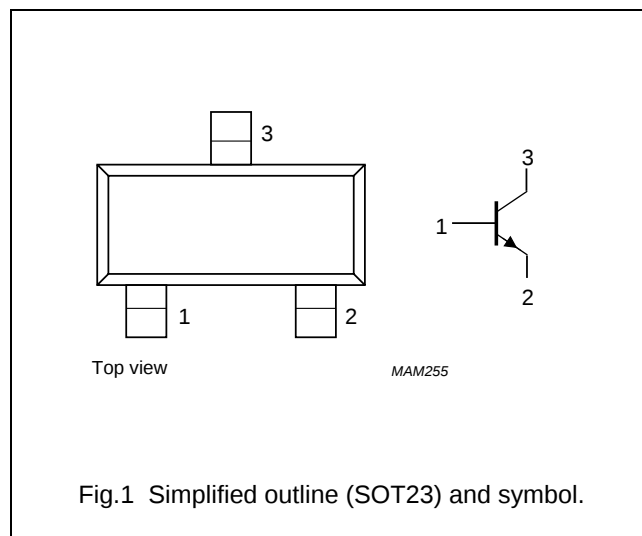
TYPE NUMBER	MARKING CODE ⁽¹⁾
PMBT5551	*G1

Note

- * = p : Made in Hong Kong.
* = t : Made in Malaysia.
* = W : Made in China.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



ORDERING INFORMATION

TYPE NUMBER	PACKAGE		
	NAME	DESCRIPTION	VERSION
PMBT5551	–	plastic surface mounted package; 3 leads	SOT23

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	–	180	V
V_{CEO}	collector-emitter voltage	open base	–	160	V
V_{EBO}	emitter-base voltage	open collector	–	6	V
I_C	collector current (DC)		–	300	mA
I_{CM}	peak collector current		–	600	mA
I_{BM}	peak base current		–	100	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25\text{ °C}$	–	250	mW
T_{stg}	storage temperature		–65	+150	°C
T_j	junction temperature		–	150	°C
T_{amb}	operating ambient temperature		–65	+150	°C

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th(j-a)}$	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

$T_{amb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I_{CBO}	collector-base cut-off current	$I_E = 0; V_{CB} = 120\text{ V}$	–	50	nA
		$I_E = 0; V_{CB} = 120\text{ V}; T_{amb} = 100\text{ }^{\circ}\text{C}$	–	50	μA
I_{EBO}	emitter-base cut-off current	$I_C = 0; V_{EB} = 4\text{ V}$	–	50	nA
h_{FE}	DC current gain	$V_{CE} = 5\text{ V}$; (see Fig.2)			
		$I_C = 1\text{ mA}$	80	–	
		$I_C = 10\text{ mA}$	80	250	
		$I_C = 50\text{ mA}$	30	–	
V_{CEsat}	collector-emitter saturation voltage	$I_C = 10\text{ mA}; I_B = 1\text{ mA}$	–	150	mV
		$I_C = 50\text{ mA}; I_B = 5\text{ mA}$	–	200	mV
V_{BEsat}	base-emitter saturation voltage	$I_C = 10\text{ mA}; I_B = 1\text{ mA}$	–	1	V
		$I_C = 50\text{ mA}; I_B = 5\text{ mA}$	–	1	V
C_c	collector capacitance	$I_E = I_E = 0; V_{CB} = 10\text{ V}; f = 1\text{ MHz}$	–	6	pF
C_e	emitter capacitance	$I_C = I_C = 0; V_{EB} = 500\text{ mV}; f = 1\text{ MHz}$	–	30	pF
f_T	transition frequency	$I_C = 10\text{ mA}; V_{CE} = 10\text{ V}; f = 100\text{ MHz}$	100	300	MHz
F	noise figure	$I_C = 200\text{ }\mu\text{A}; V_{CE} = 5\text{ V}; R_S = 2\text{ k}\Omega;$ $f = 10\text{ Hz to }15.7\text{ kHz}$	–	8	dB

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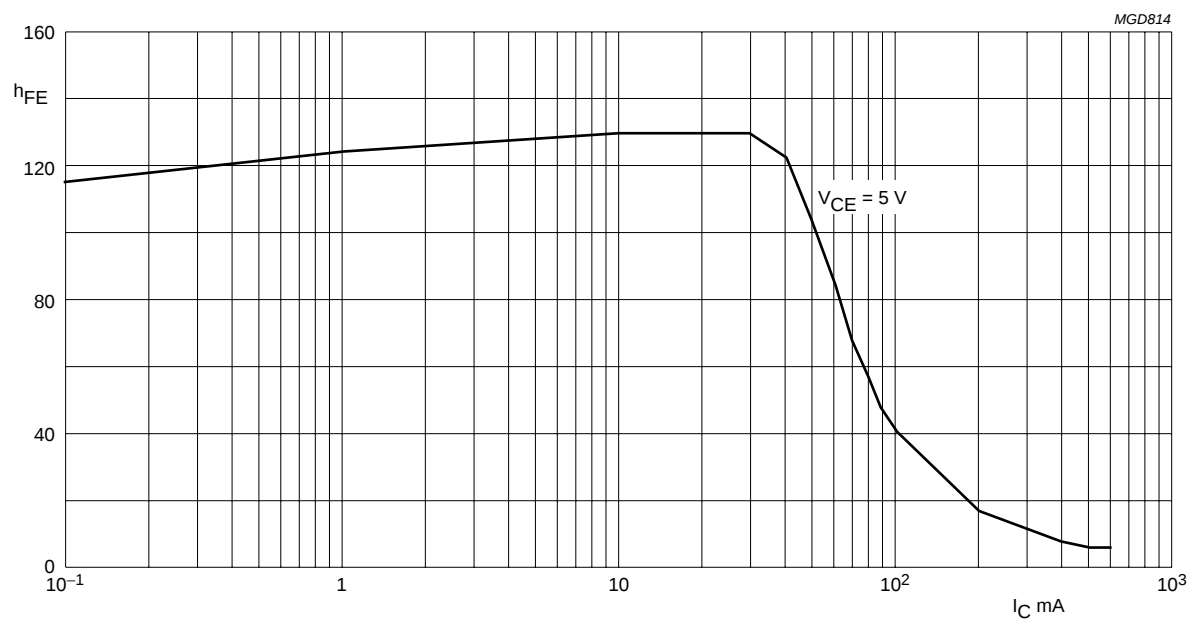


Fig.2 DC current gain; typical values

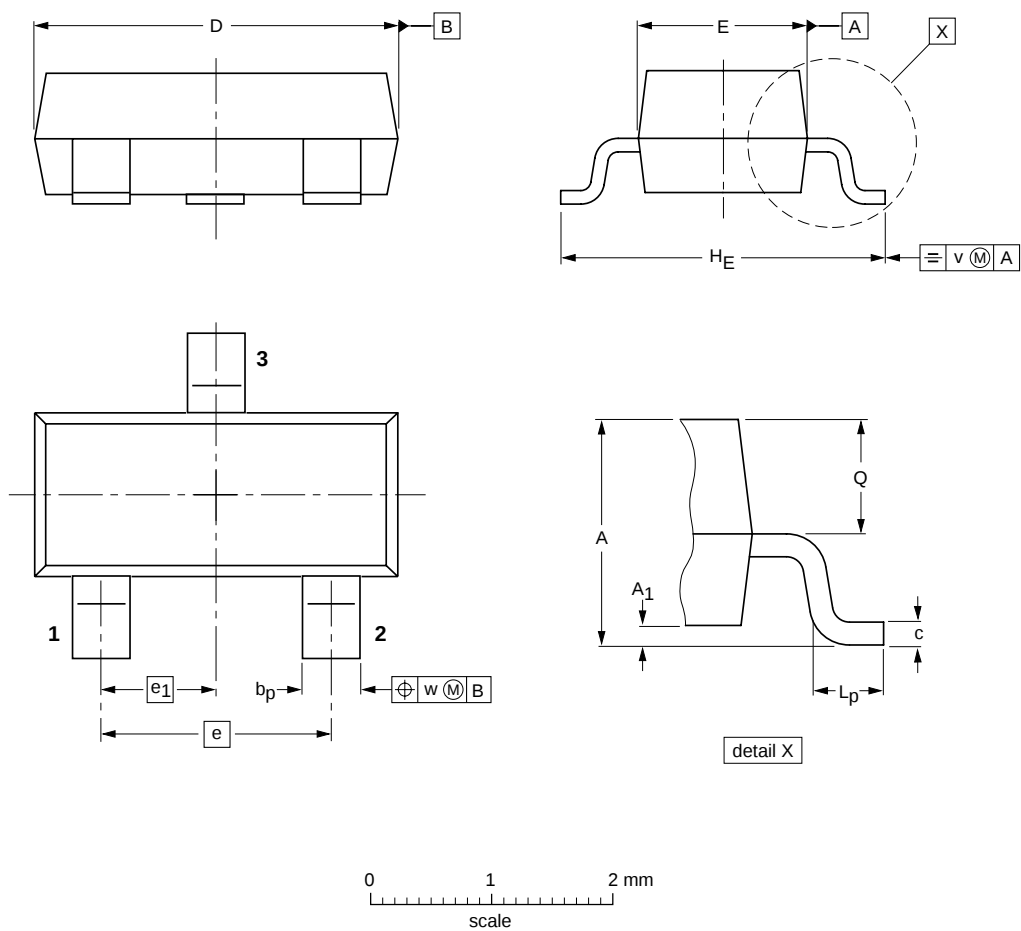
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PACKAGE OUTLINE

Plastic surface-mounted package; 3 leads

SOT23



DIMENSIONS (mm are the original dimensions)

UNIT	A	A ₁ max.	b _p	c	D	E	e	e ₁	H _E	L _p	Q	v	w
mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT23		TO-236AB				04-11-04 06-03-16